Electronic A	cknowledgement Receipt			
EFS ID:	10338810 10550326			
Application Number:				
International Application Number:				
Confirmation Number:	2088			
Title of Invention:	Intelligent data storage and processing using fpga devices  Roger D. Chamberlain			
First Named Inventor/Applicant Name:				
Customer Number:	70119			
Filer:	Benjamin Lowell Volk/Laurie Poss			
Filer Authorized By:	Benjamin Lowell Volk			
Attorney Docket Number:	53047-57365			
Receipt Date:	20-JUN-2011			
Filing Date:	09-JAN-2007			
Time Stamp:	12:09:38			
Application Type:	U.S. National Stage under 35 USC 371			

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Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)			
1	Fee Worksheet (SB06)	fee-info.pdf	30248	no	2			
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### National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

## New International Application Filed with the USPTO as a Receiving Office

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